

07/14/03  
16638 U.S. PTO

17410 U.S. PTO  
10/621253  
07/14/03

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: Sudhakar Kale et al.

Title: STRUCTURAL REGULARITY EXTRACTION AND FLOORPLANNING IN DATAPATH CIRCUITS USING  
VECTORS

Attorney Docket No.: 884.142US2

**PATENT APPLICATION TRANSMITTAL**

**Mail Stop Patent Application**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items and information (as indicated with an "X"):

- ☒ Return postcard.
- ☒ **DIVISIONAL** of prior Patent Application No.(09/435,112) (under 37 CFR ' 1.53(b)) comprising:
  - ☒ Specification (34 pgs, including claims numbered 1 through 26 and a 1 page Abstract).
  - ☒ Formal Drawing(s) (28 sheets).
  - ☒ Copy of signed Combined Declaration and Power of Attorney (20 pgs) from prior application.
  - ☒ Incorporation by Reference: *The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.*
  - ☒ Check in the amount of \$1278.00 to pay the filing fee.
- ☒ Prior application is assigned of record to Intel Corporation.
- ☒ Information Disclosure Statement (2 pgs), Form 1449 (2 pgs )  
References NOT enclosed, cited in prior application.

The filing fee has been calculated below as follows:

	No. Filed	No. Extra	Rate	Fee
TOTAL CLAIMS	26-20	6	x 18.00 =	\$108.00
INDEPENDENT CLAIMS	8-3	5	x 84.00 =	\$420.00
[ ] MULTIPLE DEPENDENT CLAIMS PRESENTED				\$0.00
BASIC FEE				\$750.00
TOTAL				\$1278.00

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Date of Deposit: July 14, 2003

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to Mail Stop Patent Application, The Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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